TF0103 Digitally Tunable LC Filters

30MHz – 90MHz – typical performance



I. General & Electrical Requirements

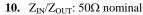
- 1. Tuned Center Frequency Range $(F_{COM})^{Note 1}$:
 - $F_{MIN} = 30MHz$ to $F_{MAX} = 90MHz$ in 250-steps, $F_{STEP} = 240kHz$
- 2. Passband @ 3dB: F_{SIG} ±200kHz
- 3. Passband Insertion Loss: ≤ 3.6 dB
- **4.** Passband Variation (peak-valley): ≤ 0.3 dB
- 5. Input/Output VSWR (within the F_{SIG} Bandwidth into 50Ω): < 2.0:1
- **6.** Absolute Stop Band Attenuation:

 F_{SIG} ±10%: 16dB minimum F_{SIG} ±15%: 22dB minimum F_{SIG} ±20%: 27dB minimum

1.6MHz to $\frac{1}{2}$ F_{TUNE}: 40dB minimum 2x F_{SIG} to < 750MHz: 35dB minimum 750MHz to 1.2GHz: 25dB minimum 1.2GHz to 2.0GHz: 15dB minimum

- 7. In-Band IIP3: +45dBm minimum
- **8.** In Band RF Power Handling: ≤ 1.25 -watts (+31dBm) CW

9. Out of Band RF Power Handling: 5-watts (+37dBm) CW, $\geq \pm 10\%$ from F_{TUNE}



11. Tuning Method:

Digital Control: 250-steps, 8-bit parallel Tuning Speed: $< 25\mu sec$

12. DC Power:

$$\begin{split} &V_1{:} + 5V_{DC} \pm 5\% \\ &I_1{:} < 275 \text{mA} \\ &V_2{:} \ 100V_{DC} \pm 5\% \\ \end{split} \label{eq:V1} \xrightarrow{\text{Note 2}}$$

 I_2 : 1.5*mA* typical

Note 1:

 $F_{SIG} = Frequency of the signal,$ Where; F_{COM} is the target command frequency that the filter will be directed to $F_{COM} = Integer((F_{SIG}, F_{MN})/F_{SIGP})^* V_{SIEP} + F_{MN}$

Note 2:

 $V_2 = 100V$, the filter command and twne frequencies are set up with 100V applied and the filter is fully compliant to these specification. For $V_2 = 50V$ (I2 = 15mA), the filter will be functional but the filter command frequency may have greater error. Power handling and linearity will be degraded.

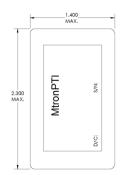
II. Environmental & Physical Requirements

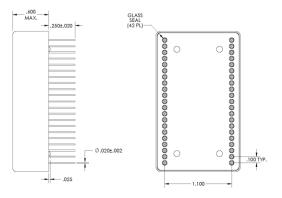
1. Temperature Range:

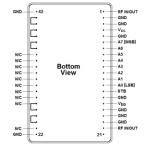
Operating: -40°C to +85°C Storage: -45°C to +90°C 2. Package

Size: 2.300" (L) x 1.100: (W) x 0.600" (H)

Style: 42-pin thru-hole







PIN	DESCRIPTION
RF IN/OUT	RF PORT
GND	GROUND
Vcc	+5V
A7 [MSB]	TUNING BIT
A6	TUNING BIT
A5	TUNING BIT
A4	TUNING BIT
A3	TUNING BIT
A2	TUNING BIT
A1	TUNING BIT
A0 [LSB]	TUNING BIT
STB	STROBE, ACTIVE LOW
V _{bb}	+100V
N/C	NO CONNECT

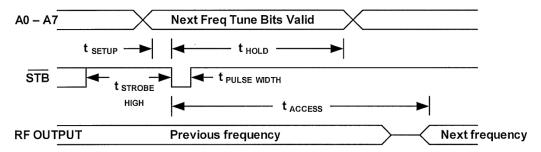
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III. Interface Timing:

Input Control Timing



t _{SETUP} = 200 ns (min)

 $t_{HOLD} = 6 \mu S (min)$

 $t_{STROBE\ HIGH} = 25\ \mu S\ (min)$

t PULSE WIDTH = 20 ns (min)

 $t_{ACCESS} = 25 \mu S (max)$

DC Control Interface Characteristics

Symbol	Parameter	Condition	Min	Max	Units
V _{IL}	Input Low Voltage	Control signals except A0 - A7	0.0	0.2 Vcc	٧
V _{IH}	Input High Voltage	Control signals except A0 - A7	0.7 Vcc	Vcc	٧
V _{IL}	Input Low Voltage	A0 - A7	0.0	0.15 Vcc	٧
V _{IH}	Input High Voltage	A0 - A7	0.7 Vcc	Vcc	٧

IV. Data Sheet Revision:

Date	Rev.	Author	Details of Revision		
06/25/13	-	BRM	Original Draft.		